UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 6,916,182 B2 Page 1 of 1

APPLICATION NO.: 10/792264
DATED: July 12, 2005
INVENTOR(S): Kenneth J. Eldredge

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 6, line 21, delete "POB" and insert -- PCB --, therefor.

In column 8, line 3, in Claim 11, after "board" insert --, --.

In column 8, line 45, in Claim 20, after "wherein the" insert -- electrical --.

Signed and Sealed this

Twentieth Day of October, 2009

David J. Kappas

David J. Kappos

Director of the United States Patent and Trademark Office